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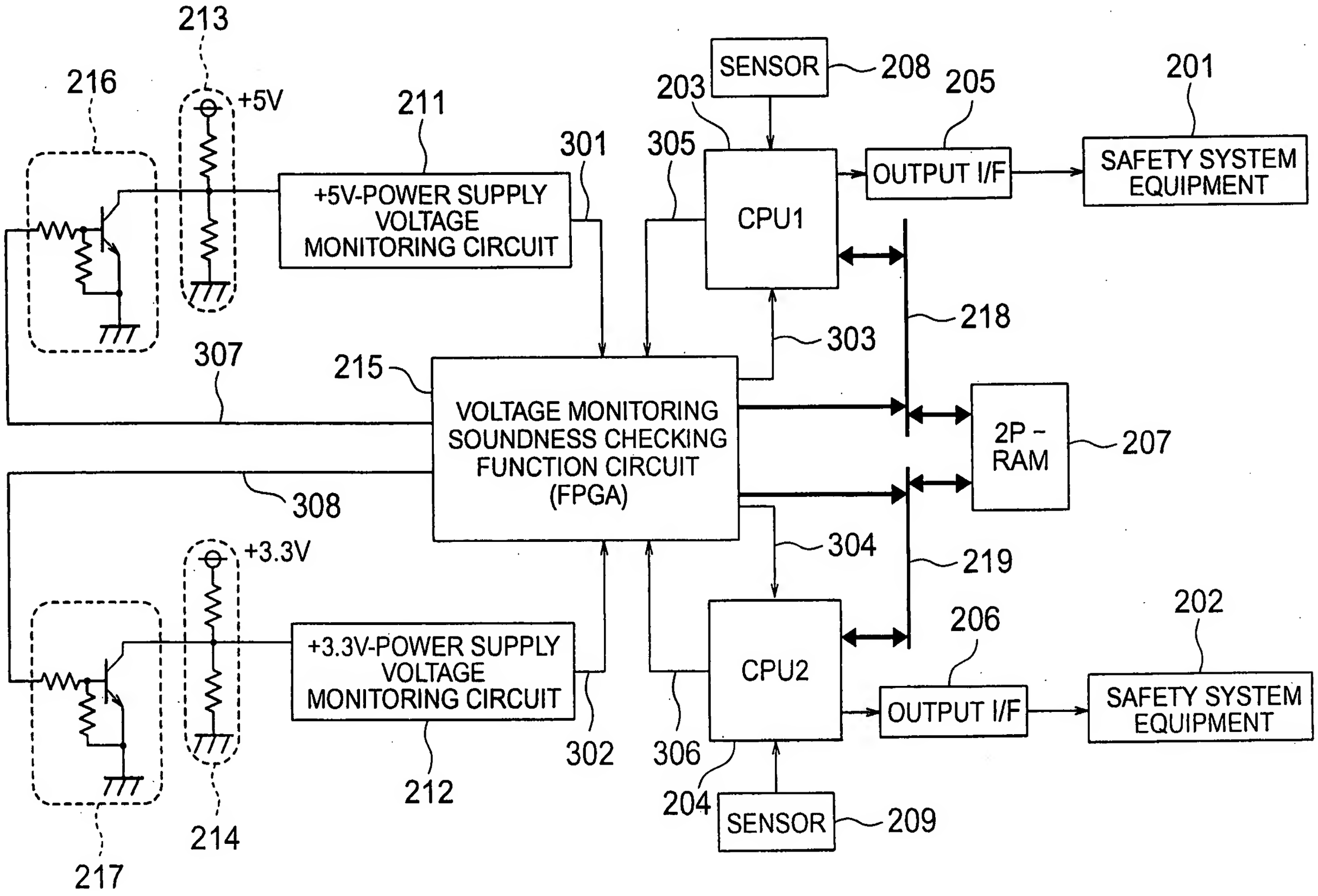
FIG. 31



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Inventor: Akihiro CHIDA
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FIG. 32

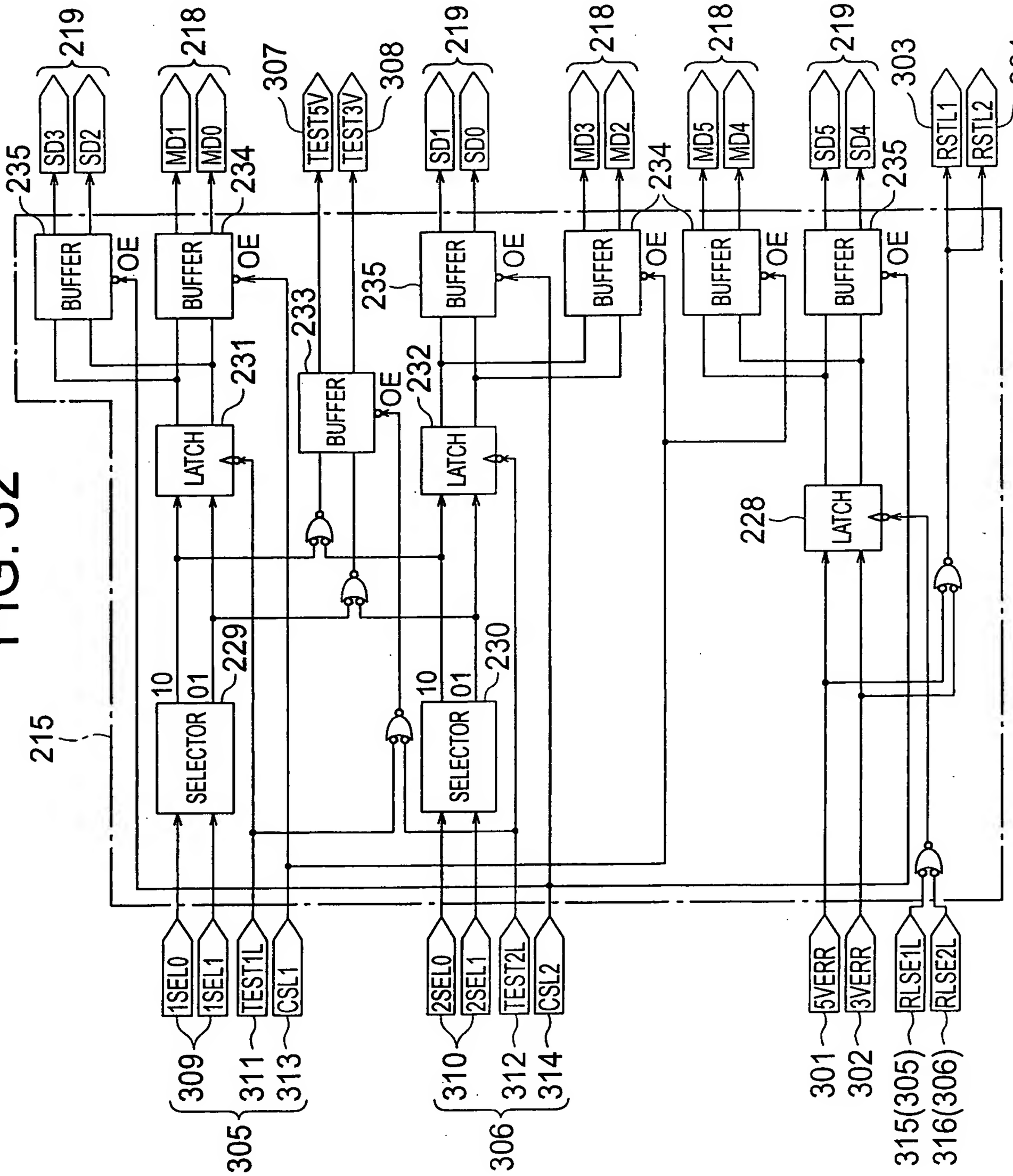

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FIG. 33

DATA BIT	MD5 (SD5)	MD4 (SD4)	MD3 (SD3)	MD2 (SD2)	MD1 (SD1)	MD0 (SD0)
MEANING OF VALUE	<ABNORMAL VOLTAGE CRITERIAL VALUE> MD5 (SD5) : 0→3.3V ABNORMAL VOLTAGE 1→NORMAL VOLTAGE MD4 (SD4) : 0→5V ABNORMAL VOLTAGE 1→NORMAL VOLTAGE		<SELECTED VOLTAGE VALUE FOR CHECKING SOUNDNESS (OF CPU ON THE OTHER SIDE)>		<SELECTED VOLTAGE VALUE FOR CHECKING SOUNDNESS (OF CPU ON OWN SIDE)>	

MD3 (SD3)	MD2 (SD2)	SELECTED VOLTAGE
0	1	3.3V
1	0	5V
OTHER THAN ABOVE-MENTIONED COMBINATION		NOT SELECTED

MD1 (SD1)	MD0 (SD0)	SELECTED VOLTAGE
0	1	3.3V
1	0	5V
OTHER THAN ABOVE-MENTIONED COMBINATION		NOT SELECTED

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